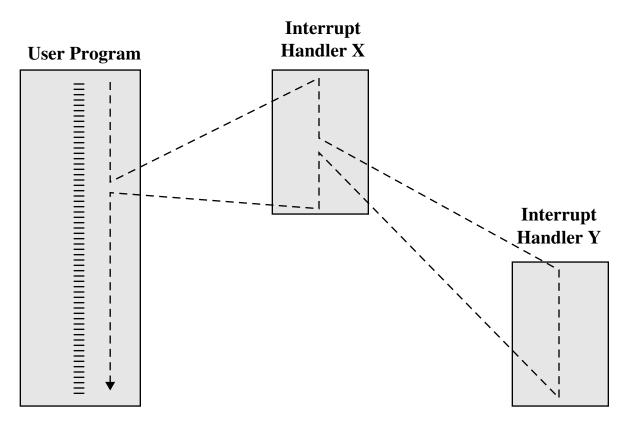


## (a) Sequential interrupt processing



(b) Nested interrupt processing

## **Figure 1.12 Transfer of Control with Multiple Interrupts**