Virtual Address

Page Number

Page Table Entry

PMOther Control Bits Frame Number

(a) Paging only

Offset

Virtual Address

Segment Number	Offset
----------------	--------

Length

Segment Table Entry

P MOther Control Bits

Segment Base

(b) Segmentation only

Virtual Address				
Segment Number	Page Number		Offset	
Segment Table Entry				
Other Control Bits	Length	Segment Base		
Page Table Entry				
P MOther Control Bits	Frame Numl	ber		P= present bit

M = Modified bit

(c) Combined segmentation and paging

## **Figure 8.2 Typical Memory Management Formats**