Lecture 4

Performance Evaluation Models



Performance Model

Inputs & outputs

performance model workload SW HW params

params params

Solve model

Solution methods

performance measures

- depend on model
- trivial: rules of thumb
- complex: analytical, simulation, benchmarks

Example: Database Server

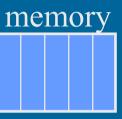
Query transactions

0.5 tps

Multiprogrammed OS







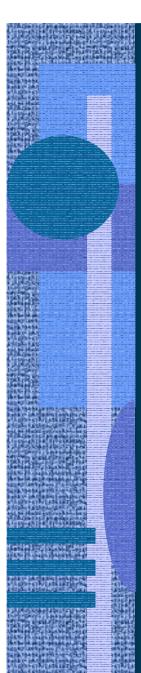


5 partitions (I.e., max 5 transactions processed at the same time)

- What if 1.0 tps?
 - need faster CPU? or more memory?
- Queues? Resources? Active? Passive?
- Use of resources? service time?



- Acquire memory partition
 - queue for memory?
- Use CPU
 - queue for CPU?
- until
 - I/O operation: use (and queue for) disk
 - timeslice expires: give CPU to next job
 - transaction completes: release mem & depart



Active resource

- Server, device
- Gives service
- Must have in possession during service
- Kept only during service
- Waiting queue or line
- Speed or rate of service, service time
 - parameter to model?

E.g., aver 4.6 ms

- aver. value? distribution?

• Fig. 3.2 [Men 94]

CPU, Disks



- Allocated, reserved
- Deallocated, freed
- Waiting queue or line
- Must have in order to proceed

Memory

- Kept until deallocated
 - difficult for Markov Chain based analytical solutions
 - trouble: simultaneous resource possession
- Figs 3.3 & 3.4

Queueing Network (QN)

Network of Queues

- Operational Analysis
- Open Queueing Network
 - Database server: Fig. 3.5 [Men 94]

Utilization

Total

time

observation

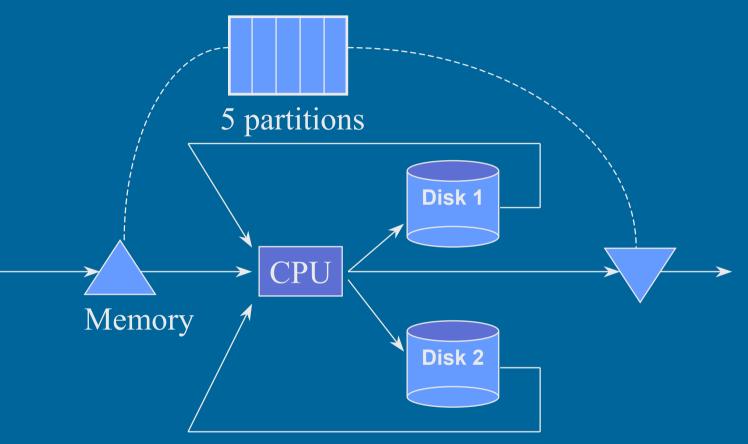
- system, arrivals
- transitions, transition probabilities
- queues, queue lengths
- subsystem, easy to solve!
- Service demands (D_i)
- $D_i = (U_i * T) / C_0$

- Tbl 3.1

$$D_i = V_i S_i$$

Completions from system

Open Queueing Network



Service Demand D_i



Total observation time

$$D_i = (U_i * T) / C_0$$

 $= U_i / (C_0 / T) = U_i / X_0$

Completions from system

$$= V_i S_i$$

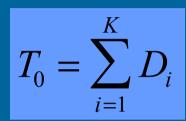
Device Service Time

System throughput

Device Visit Ratio = Nr of visits to device







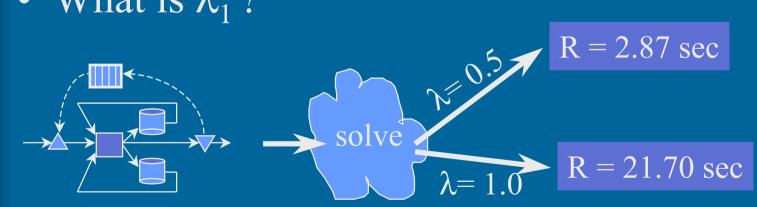
- Minimum response time:
- Queueing time?

$$T = T_0 + W_{mem} + W_{cpu} + W_{disk1} + W_{disk2}$$

- Average arrival rate $\lambda = 0.5$ tps
- Maximum degree of multiprogramming
 - how many jobs in subsystem?
 - $-N^{\text{max}} = 5$

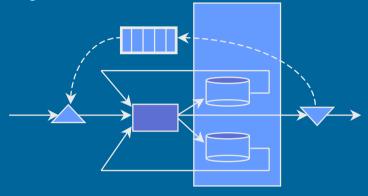
How to Solve System Model?

- Depends on arrival rate!
 - Tbl 3.2
- Easy to solve with light load
- More complex to solve with heavy load
- What is λ_1 ?



Disk Subsystem

- I/O channels? SCSI?
- Heads of strings?
- Device controllers
- Disk cache
- File access protocol
- Rotation speed?



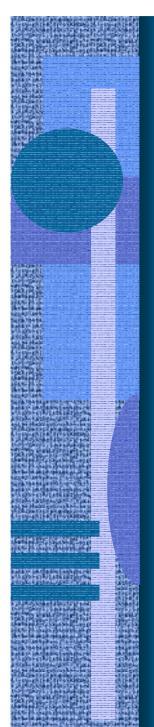


OK or not OK?

Interactive Systems

(osituskäyttö systeemit)

- People are part of model
- Terminals, work stations, ...
- Response time (R), Think time (Z)
- Fig. 3.6
- Tbl 3.7
- How to solve?
 - depends on number of terminals
 - Tbl 3.4



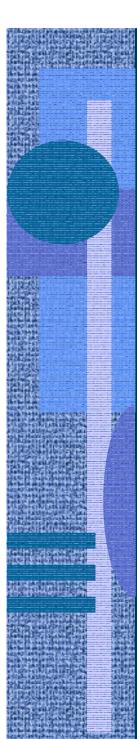
Batch Systems

(eräajosysteemit)

- No people
- Closed system
- Fixed number of multiprogramming level
- Tbl 3.5



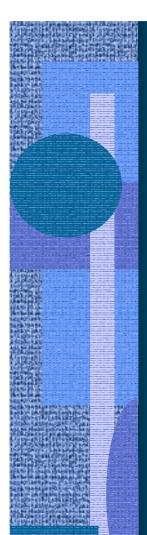
- More difficult than before
 - More parameters to estimate
 - More complex to solve
- Gives more usable information
- Open model: class arrival rates
- Closed model: class populations
- Tbl 3.6



Aggregating Classes

(luokkien yhdistely)

- Combine classes to make model simpler
 - aggregate all uninteresting job classes together?
- To make parameter estimation simpler
- Tbls 3.7 & 3.8
- Need to compute derived parameter values for aggregate class from those of component classes
 - method varies depending on network type
 - Figs 3.9, 3.10 & 3.11



Priorities

- Priorities are used in real systems
 - CPU, disks, etc
- May be dynamically changing
- Difficult to model well
- Models with priorities are more complex to solve (than those with no priorities)
- Example with Tbl 3.9

Shared Domains

(yhteisalueet)

- Class limits or passive resources shared with other classes
 - multiprogramming level
 - memory partitions
 - Fig. 3.12

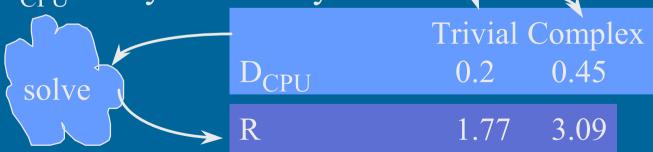
Multiple Class Model Parameters

• Tbl 3.10

Baseline Model and Modification Analysis

- Baseline: Tbl 3.11
- A: Use DBMS: Tbl 3.12
- B: Use DBMS + Optimizing compiler
 - $-\overline{\mathrm{D}_{\mathrm{CPU}}}$ down 50%
 - only for applications, 40% of CPU path length





Baseline Model and Modification Analysis (contd)

- C: DBMS + Larger DB record buffer pool (I.e., larger disk cache)
 - D_{DISK1} and D_{DISK2} down 30%
 - Tbl 3.13

Baseline Model and Modification Analysis (contd)

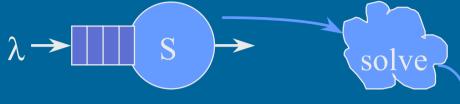
- D: Use also transaction logging for crash recovery?

 HW specs
 - disk update needs 2048 B record (38.7 msec)
 - logging only for complex transactions
 - assume each complex transaction causes one disk update, and so one log update (to DISK1)
 - D_{DISK1} up 0.0387 sec



Solution Methods

Analytical



much more complex for complex models

$$R = \frac{S}{1 - \lambda S}$$

- Approximate
 - approximate reality with simpler model
 - approximate exact solution for complex model
- Simulation
 - Monte Carlo
 - statistical analysis

